

PW2016L

## 20V Dual N-Channel MOSFET

5.5A 20V;  $R_{DS(ON)typ}=15.7\text{m}\Omega$ @4.5V,  $R_{DS(ON)typ}=20\text{m}\Omega$ @2.5V

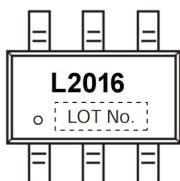
FEATURE

- TrenchFET Power MOSFET
  - Excellent RDS(on)
  - Low Gate Charge
  - High Power and Current Handing Capability
  - Surface Mount Package

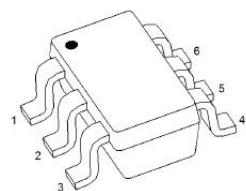
## Application

- Battery Protection
  - Load Switch
  - Power Management

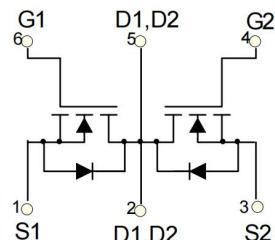
## MARKING:



SOD-23-6L



## Schematic diagram



**ABSOLUTE MAXIMUM RATINGS ( $T_a=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±10	V
Continuous Drain Current	I <sub>D</sub>	5.5	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	23	A
Power Dissipation	P <sub>D</sub>	1.5	W
Thermal Resistance from Junction to Ambient <sup>2</sup>	R <sub>QJA</sub>	83.3	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C

**MOSFET ELECTRICAL CHARACTERISTICS( $T_a=25^\circ\text{C}$  unless otherwise noted)**

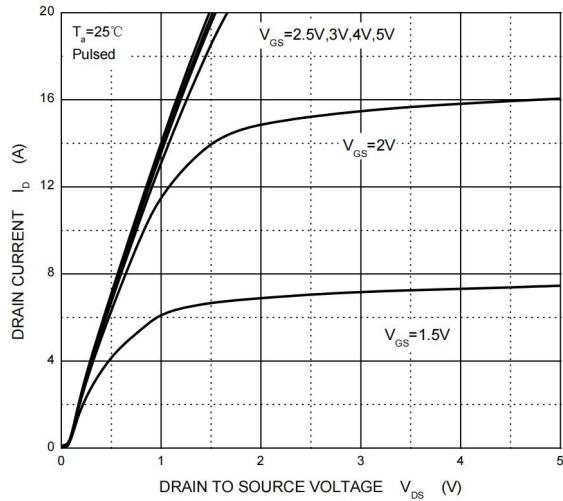
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>STATIC CHARACTERISTICS</b>						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}} = 18\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate-body leakage current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
Gate threshold voltage <sup>3</sup>	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.5	0.7	1.2	V
Drain-source on-resistance <sup>3</sup>	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 3\text{A}$		15.7	20	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 3\text{A}$		20	27	
Forward Transconductance <sup>3</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 4.5\text{A}$	5			S
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_S = 1.25\text{A}$			1.2	V
<b>DYNAMIC CHARACTERISTICS<sup>4</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 8\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		800		pF
Output Capacitance	$C_{\text{oss}}$			155		
Reverse Transfer Capacitance	$C_{\text{rss}}$			125		
<b>SWITCHING CHARACTERISTICS<sup>4</sup></b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 4\text{A}$		11		nC
Gate-Source Charge	$Q_{gs}$			2.3		
Gate-Drain Charge	$Q_{gd}$			2.5		
Turn-on delay time	$t_{d(\text{on})}$	$V_{\text{DD}} = 10\text{V}, V_{\text{GS}} = 4\text{V}$ $I_D = 1\text{A}, R_G = 10\Omega$		18		ns
Turn-on rise time	$t_r$			5		
Turn-off delay time	$t_{d(\text{off})}$			43		
Turn-off fall time	$t_f$			20		

**Notes :**

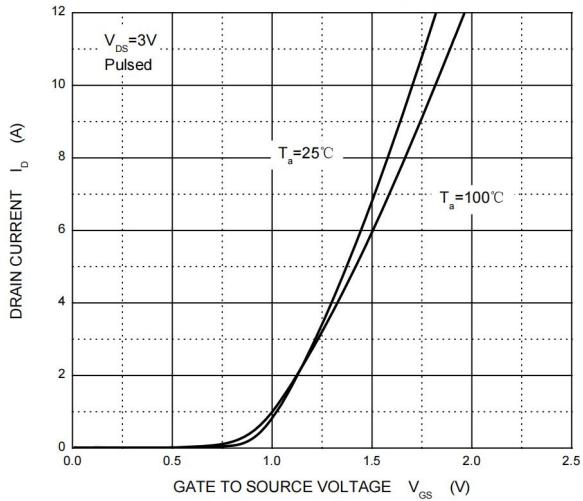
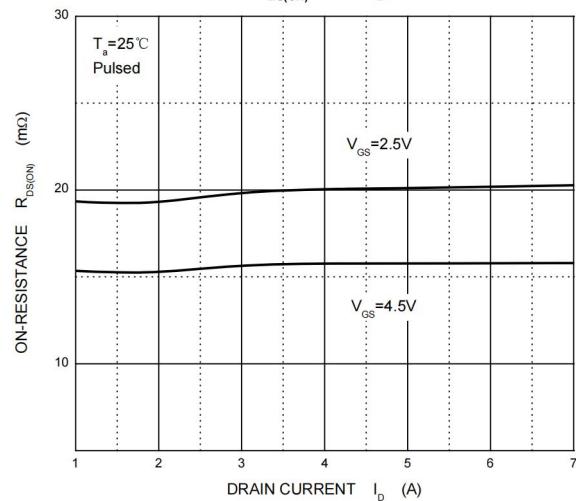
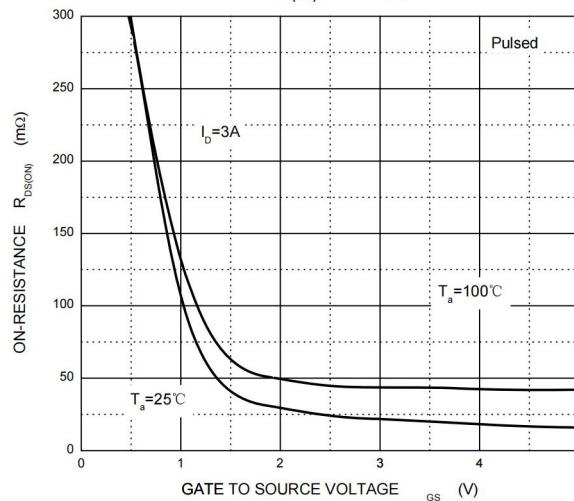
- 1.Repetitive rating : Pulse width limited by maximum junction temperature
- 2.Surface mounted on FR4 board using 1 square inch pad size, 1oz single-side copper.
- 3.Pulse test : Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 4.Guaranteed by design, not subject to production.

## Typical Characteristics

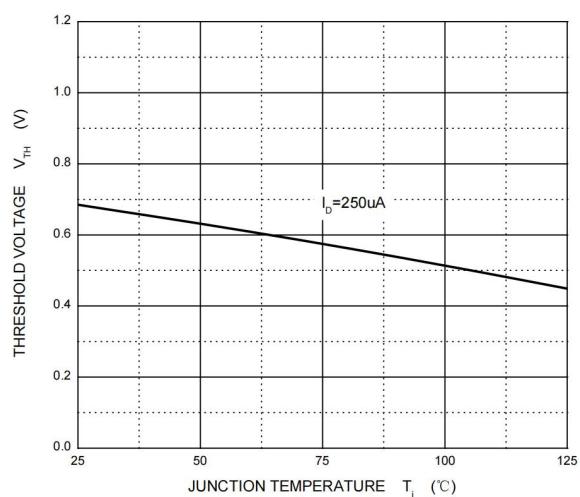
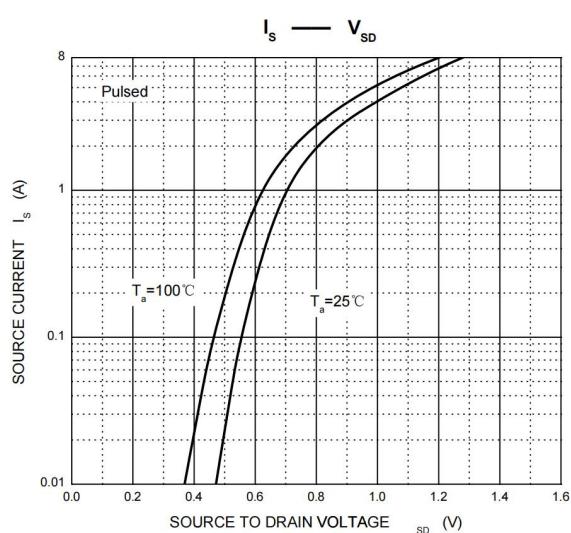
Output Characteristics

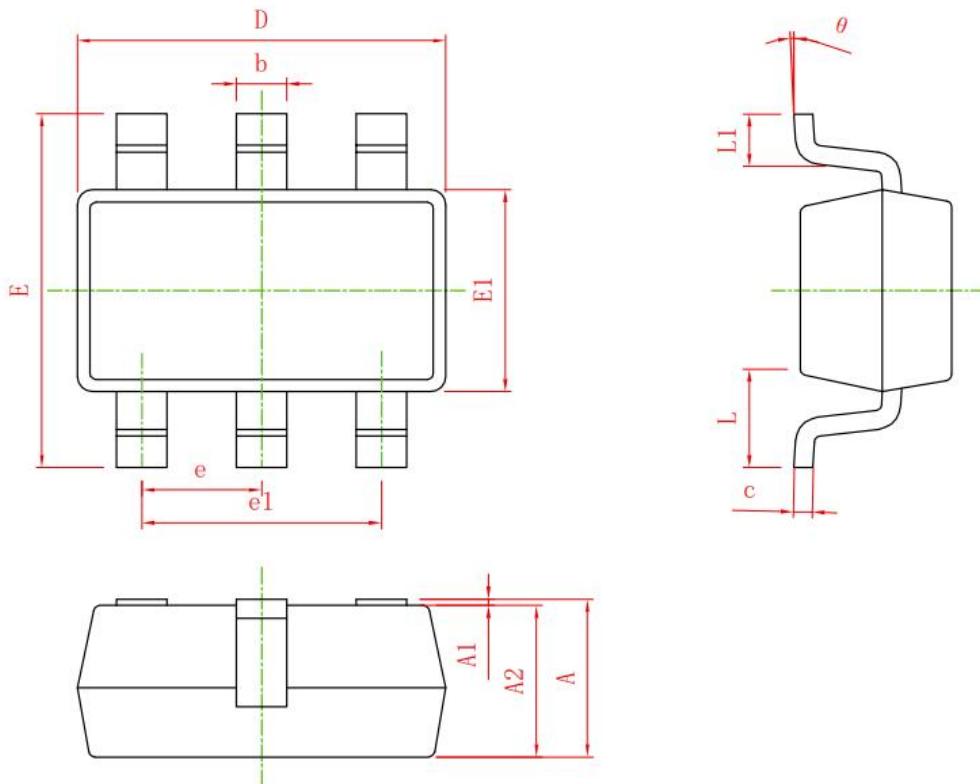


Transfer Characteristics

 $R_{DS(ON)}$  —  $I_D$  $R_{DS(ON)}$  —  $V_{GS}$ 

Threshold Voltage



**SOT-23-6L Package Outline Dimensions**

Symbol	Dimensions In Millimeters		Dimensions In Inche	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
L1	0.600REF.		0.024REF.	
$\theta$	0°	8°	0°	8°