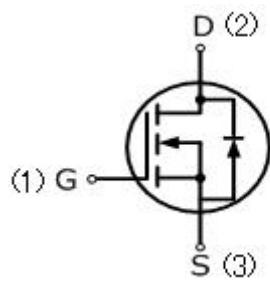


## 3N150Y

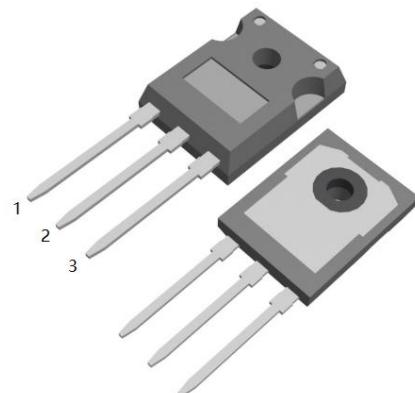
3 Amps, 1500 Volts N-CHANNEL Power MOSFET

### FEATURE

- 3A, 1500V,  $R_{DS(ON)} \text{MAX} = 8.2 \Omega$  @  $V_{GS} = 10V/1.5A$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS 2.0 Compliant



TO-247-3L



### Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	3N150Y	UNIT
Drain-Source Voltage	$V_{DSS}$	1500	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	
Continuous Drain Current	$I_D$	3	A
Pulsed Drain Current (Note 1)	$I_{DM}$	12	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	80	mJ
Reverse Diode $dV/dt$ (Note 3)	$dv/dt$	5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

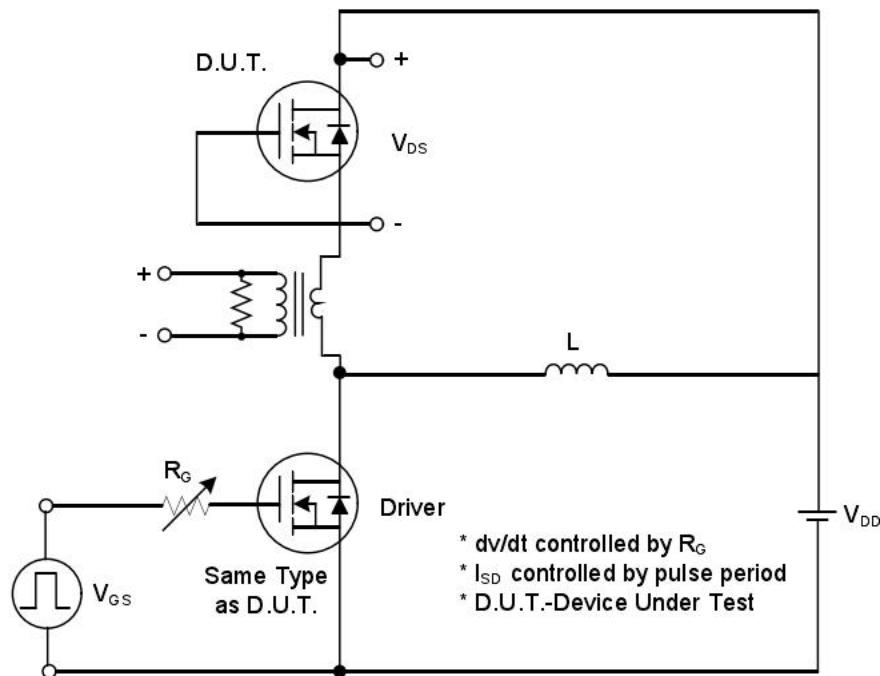
Parameter	Symbol	3N150Y	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	1	°C/W
Maximum Power Dissipation	$T_c = 25^\circ\text{C}$	$P_D$	125

Electrical Characteristics ( $T_c=25^\circ\text{C}$ ,unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	1500	—	—	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=1500\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	uA
Gate-Body Leakage Current,Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current,Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2.5	—	4.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.5\text{A}$	—	5.4	8.2	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHZ}$	—	1600	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	100	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	33	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=750\text{V}, \text{I}_D=3\text{A},$ $\text{R}_G=4.7\Omega$	—	25	—	ns
Turn-On Rise Time	$t_r$		—	48	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	57	—	ns
Turn-Off Fall Time	$t_f$		—	52	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=750\text{V}, \text{I}_D=3\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	—	36	—	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		—	9.5	—	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		—	12	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Continuous Diode Forward Current	$\text{I}_S$		—	—	3	A
Pulsed Diode Forward Current	$\text{I}_{\text{SM}}$		—	—	12	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=1.5\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.5	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=3\text{A},$ $d\text{I}_F/dt=100\text{A/us}, (\text{Note}3)$	—	255	—	ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$		—	1.1	—	uC

#### Notes

- Repetitive Rating:pulse width limited by maximum junction temperature.
- $\text{V}_{\text{DD}}=50\text{V}, L=10\text{mH}, R_g=25\Omega$ , starting  $T_j=25^\circ\text{C}$ .
- Pulse width $\leq 300\text{us}; \text{duty cycle}\leq 2\%$ .

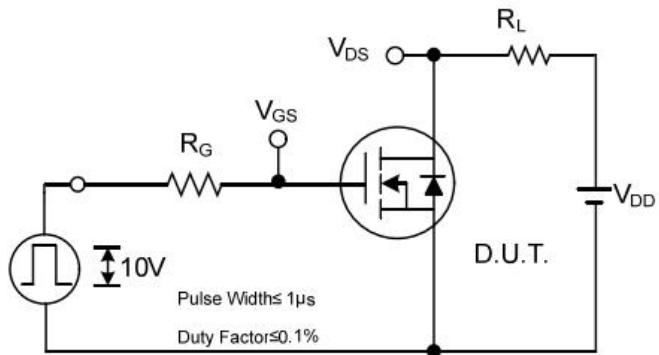
## RATING AND CHARACTERISTIC CURVES



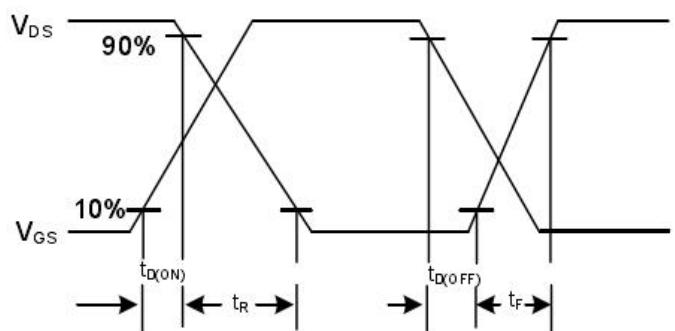
Peak Diode Recovery dv/dt Test Circuit



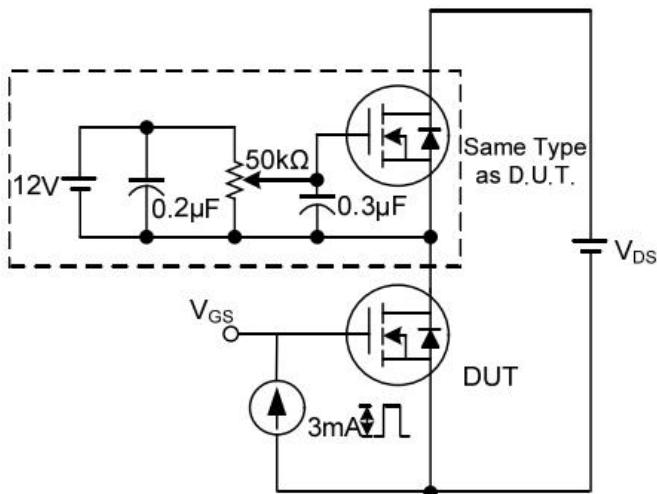
Peak Diode Recovery dv/dt Waveforms



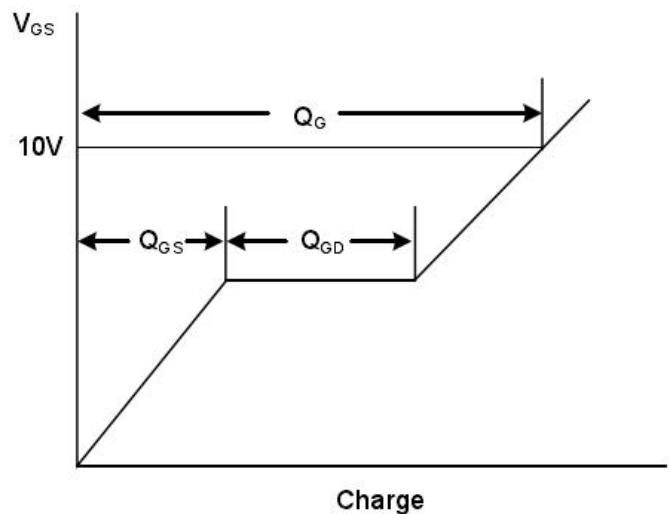
Switching Test Circuit



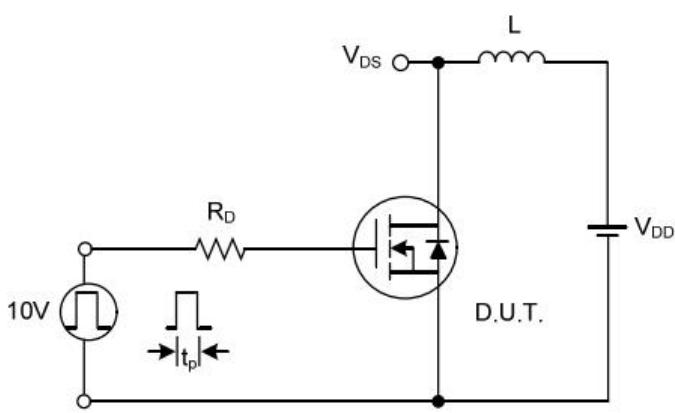
Switching Waveforms



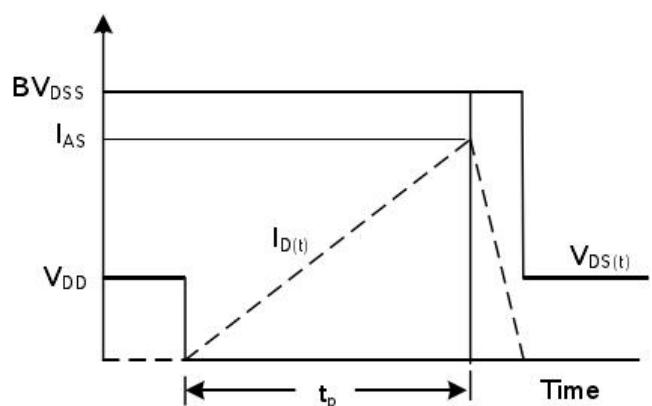
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

## RATING AND CHARACTERISTIC CURVES

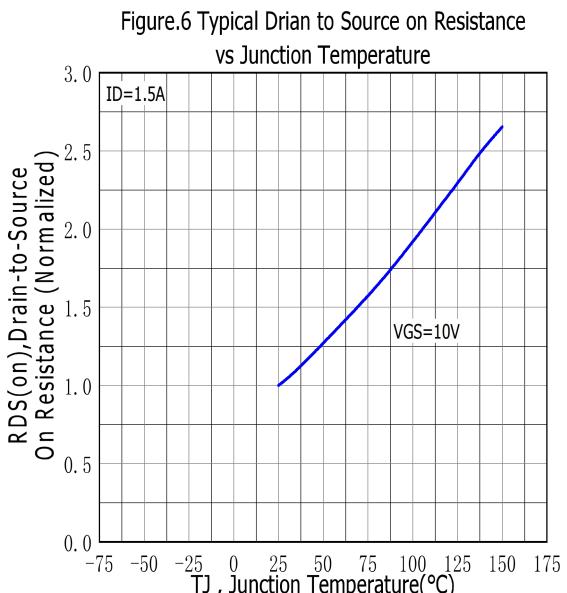
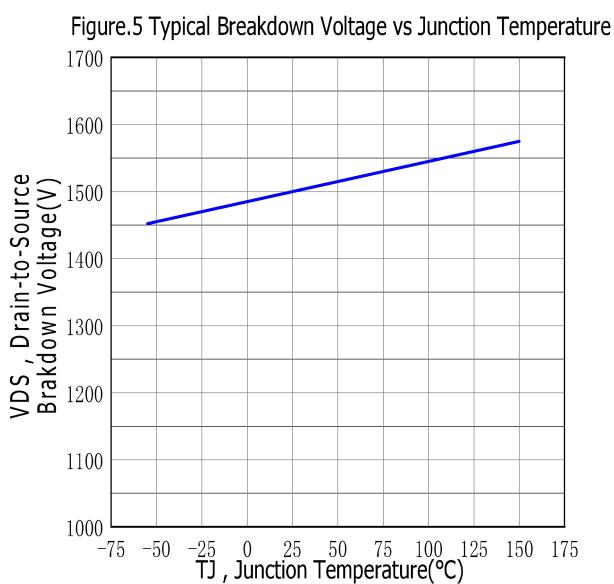
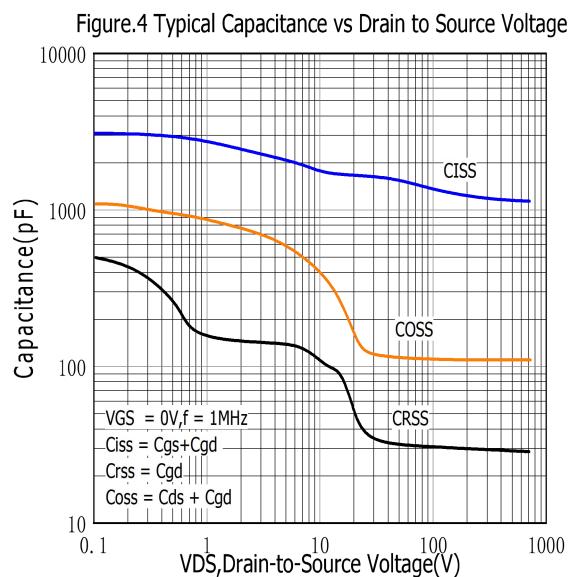
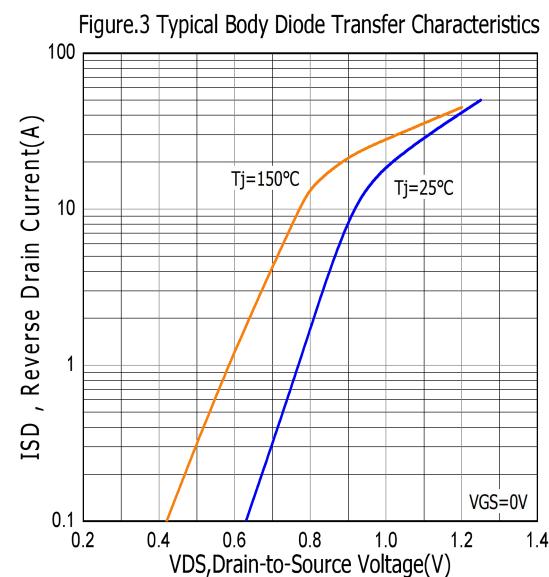
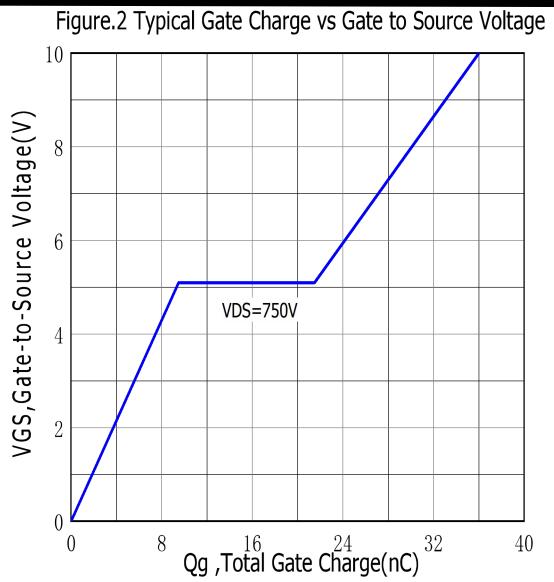
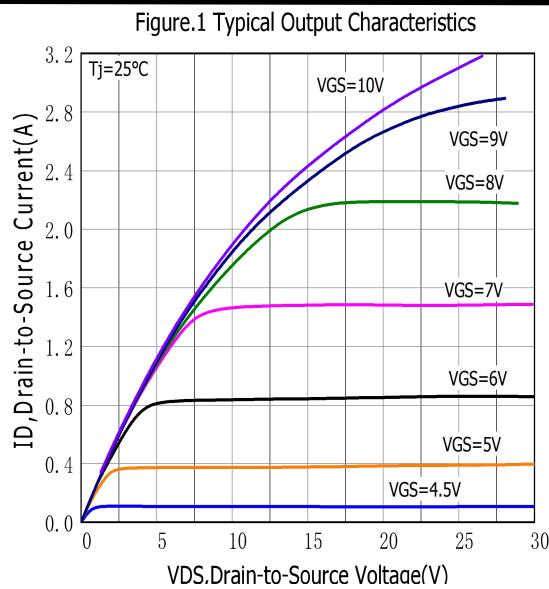


Figure.7 Maximum Forward Bias Safe Operating Area

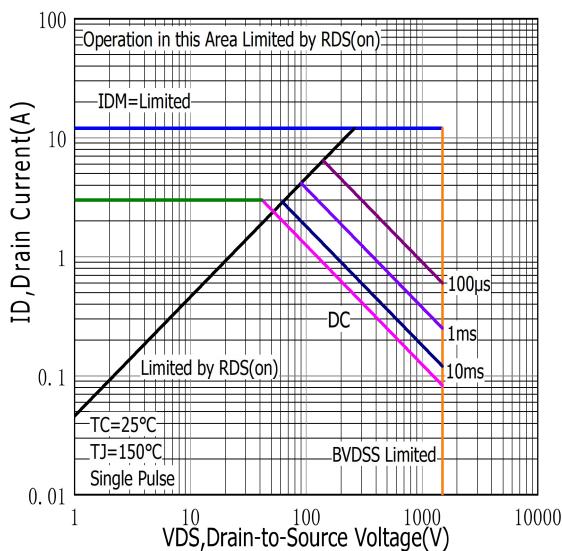


Figure.9 Maximum EAS vs Channel Temperature

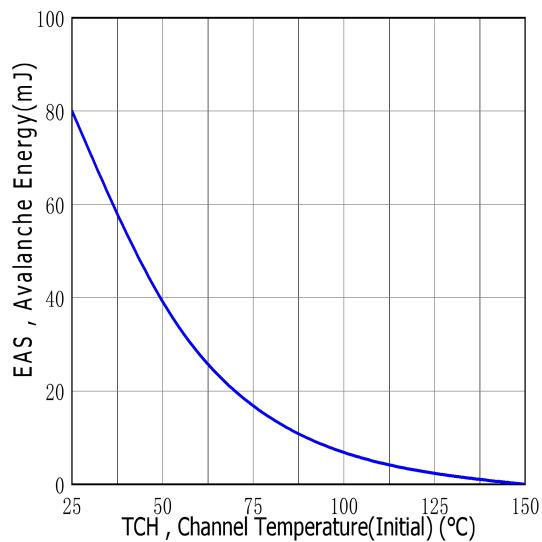


Figure.11 Maximum Effective Thermal Impedance , Junction to Case

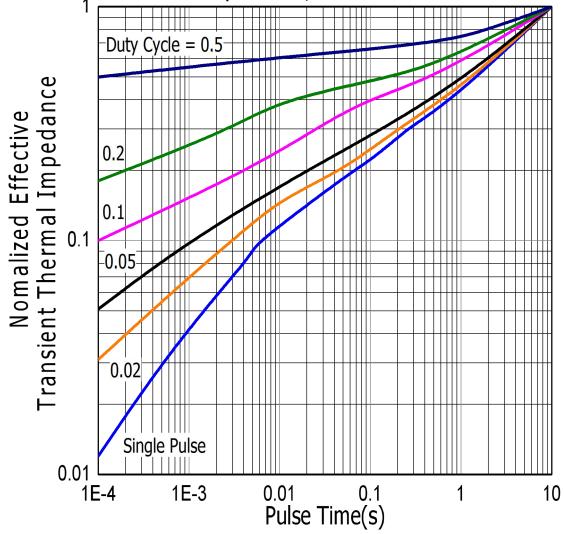


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

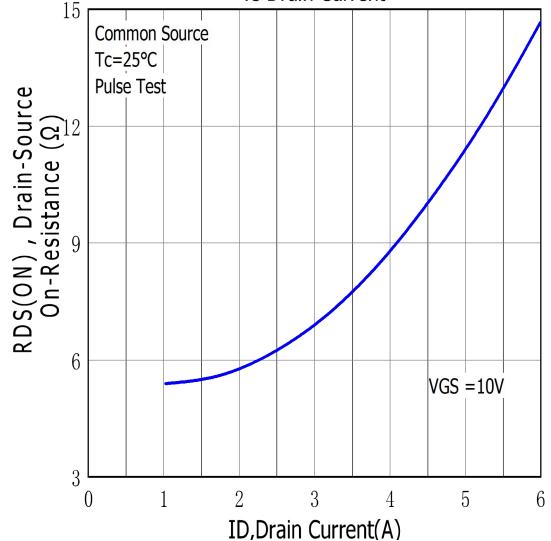


Figure.10 Typical Threshold Voltage vs Case Temperature

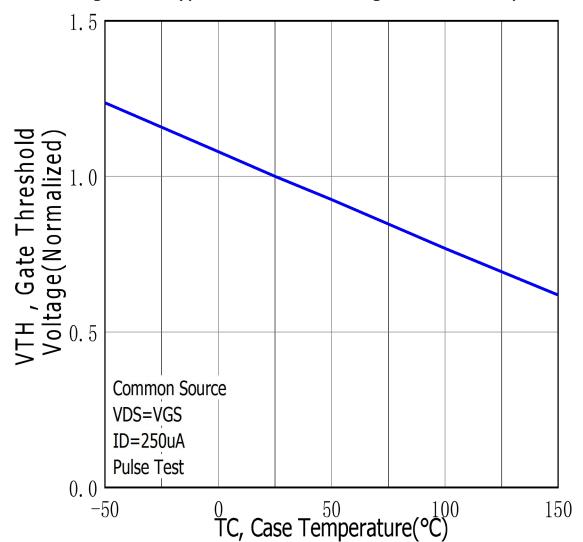
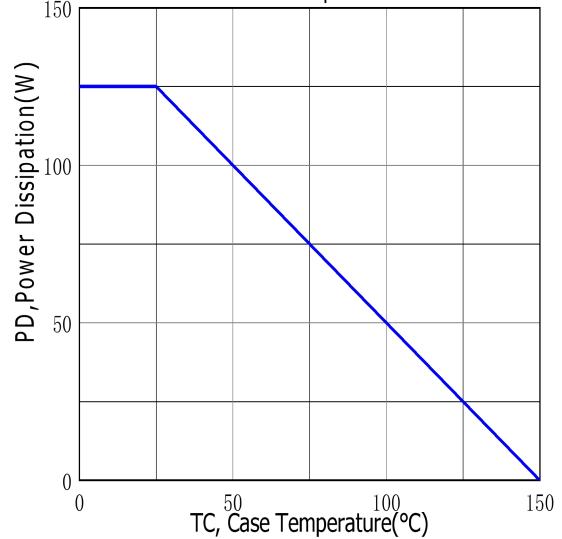
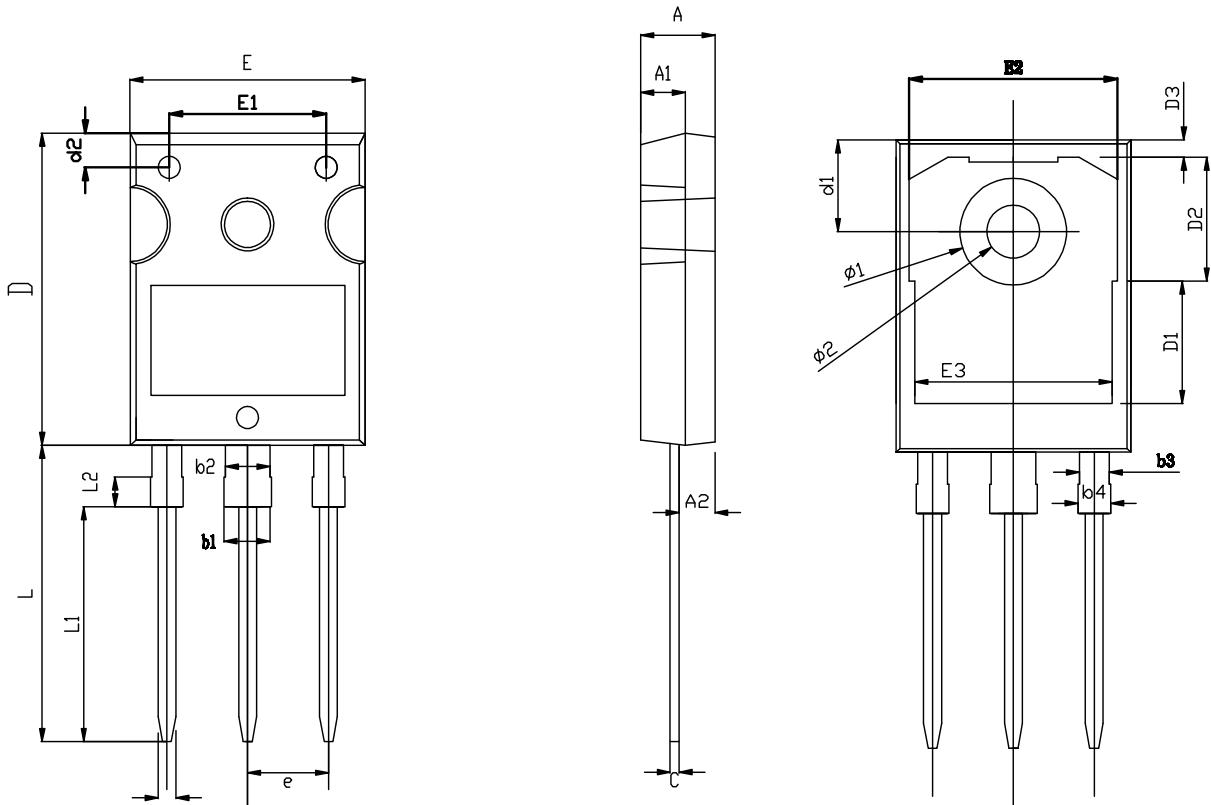


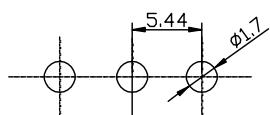
Figure.12 Maximum Power Dissipation vs Case Temperature



## TO-247-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.80	3.00	3.20
A2	2.26	2.41	2.56
b	1.10	1.20	1.30
b1	2.90	-	3.20
b2	2.90	3.00	3.10
b3	1.90	2.00	2.10
b4	2.00	-	2.20
c	0.50	0.60	0.70
D	20.80	21.00	21.20
D1		8.23	
D2		8.32	
D3		1.17	
d1	6.00	6.15	6.30
d2	2.20	2.30	2.40
E	15.60	15.80	16.00
E1		10.50	
E2		14.02	
E3		13.50	
e	5.34	5.44	5.54
L	19.72	19.92	20.12
L1		15.79	
L2		1.98	
$\phi_1$	7.10	7.19	7.30
$\phi_2$	3.50	3.60	3.70