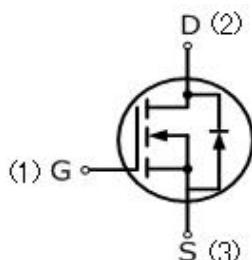


## 10N80F

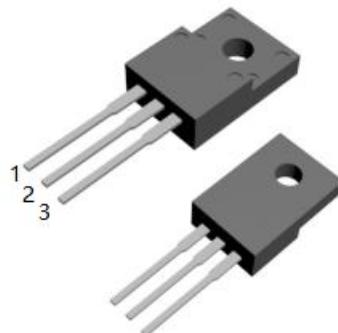
10 Amps, 800 Volts N-CHANNEL Power MOSFET

### FEATURE

- 10A, 800V,  $R_{DS(ON)} \text{MAX} = 1.15\Omega$  @  $V_{GS} = 10V/4A$
- Low gate charge
- Low  $C_{iss}$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-220F-3L



### Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	10N80F	UNIT
Drain-Source Voltage	$V_{DSS}$	800	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	
Continuous Drain Current	$I_D$	10	A
Pulsed Drain Current (Note 1)	$I_{DM}$	40	
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	780	mJ
Reverse Diode $dV/dt$ (Note 3)	$dv/dt$	5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	260	°C

Parameter	Symbol	10N80F	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	2.27	°C/W
Thermal resistance, Channel to Ambient	$R_{th(ch-a)}$	100	°C/W
Maximum Power Dissipation	$T_c = 25^\circ\text{C}$	$P_D$	W

Electrical Characteristics ( $T_c=25^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	800	—	—	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=800\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	$\mu\text{A}$
Gate-Body Leakage Current, Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current, Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
<b>On Characteristics</b>						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2	—	4	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A}$	—	1	1.15	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	—	2900	—	pF
Output Capacitance	$\text{C}_{\text{oss}}$		—	200	—	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		—	25	—	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=400\text{V}, \text{I}_D=10\text{A},$ $\text{R}_G=4.7\Omega$ (Note 3,4)	—	19	—	ns
Turn-On Rise Time	$t_r$		—	10	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	68	—	ns
Turn-Off Fall Time	$t_f$		—	23	—	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=640\text{V}, \text{I}_D=10\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$ , (Note 3,4)	—	60	—	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		—	13	—	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		—	22	—	nC
<b>Drain-Source Body Diode Characteristics and Maximum Ratings</b>						
Continuous Diode Forward Current	$\text{I}_S$		—	—	10	A
Pulsed Diode Forward Current	$\text{I}_{\text{SM}}$		—	—	40	A
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_S=10\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.5	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=10\text{A},$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$ , (Note 4)	—	200	—	ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$		—	2.2	—	$\mu\text{C}$

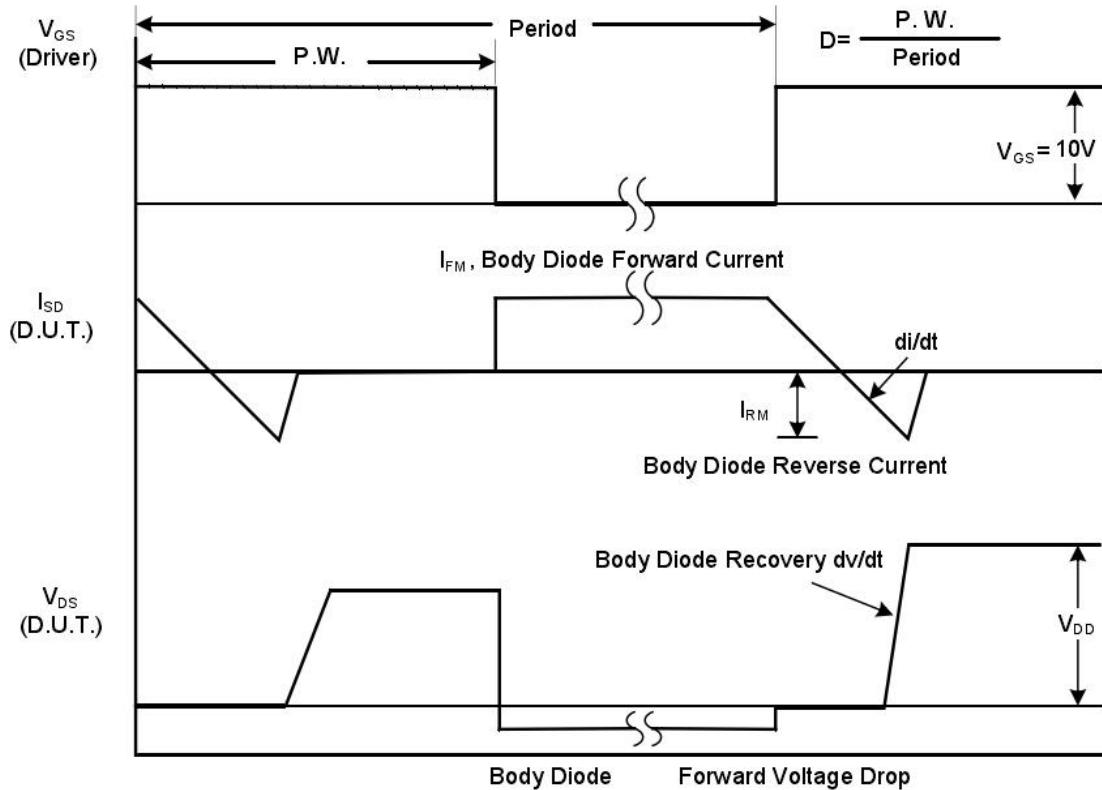
#### Notes

- Repetitive Rating: pulse width limited by maximum junction temperature.
- $\text{V}_{\text{DD}}=50\text{V}, L=10\text{mH}, R_g=25\Omega$ , starting  $T_j=25^\circ\text{C}$ .
- $\text{I}_{\text{SD}} \leq \text{I}_D, d\text{I}/dt \leq 100\text{A}/\mu\text{s}, \text{V}_{\text{DD}} \leq \text{BV}_{\text{DSS}}$ , starting  $T_j=25^\circ\text{C}$ , Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- Repetitive rating; pulse width limited by maximum junction temperature.

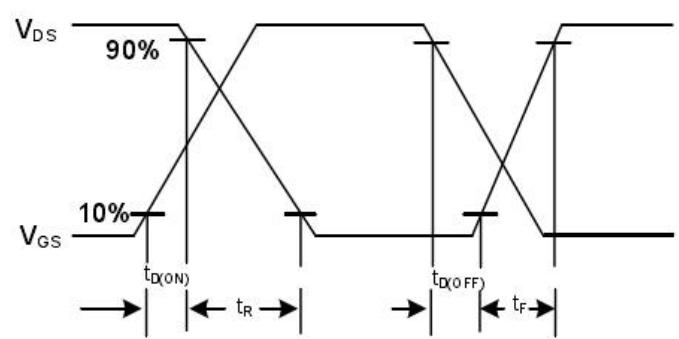
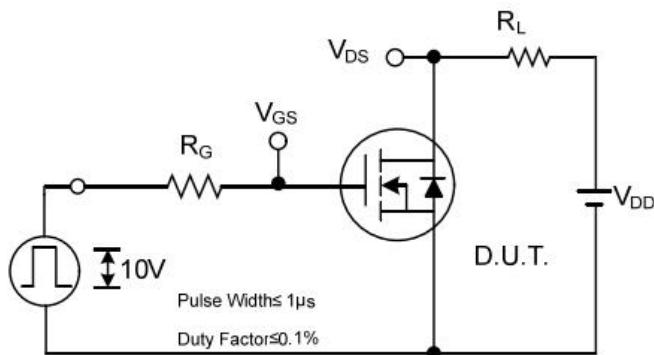
## TEST CIRCUIT AND WAVEFORM



Peak Diode Recovery dv/dt Test Circuit

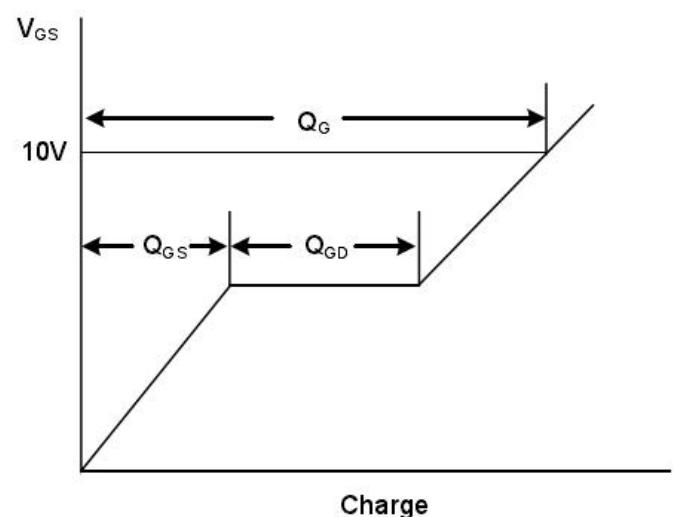
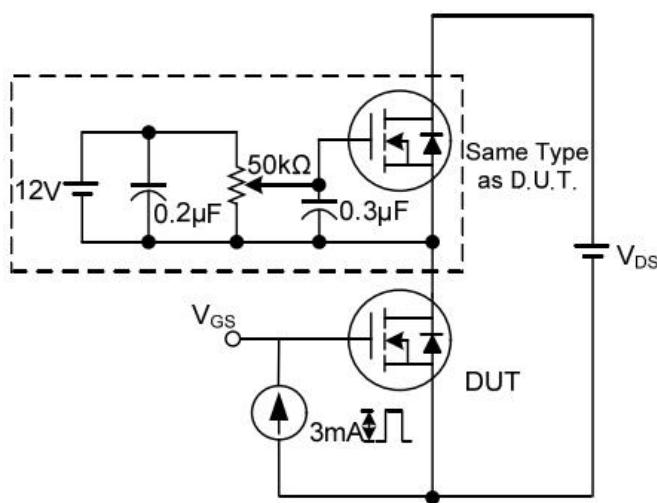


Peak Diode Recovery dv/dt Waveforms



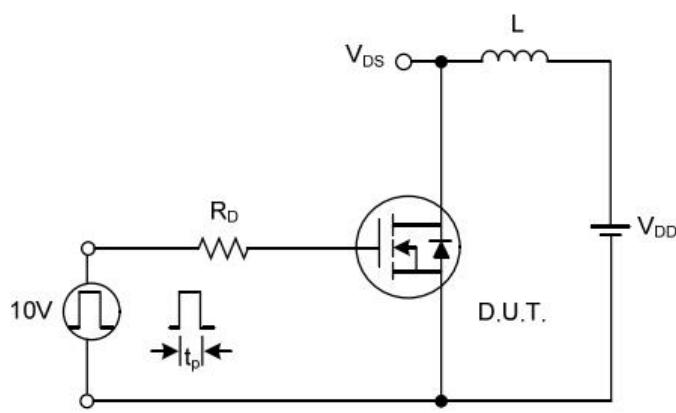
Switching Test Circuit

Switching Waveforms

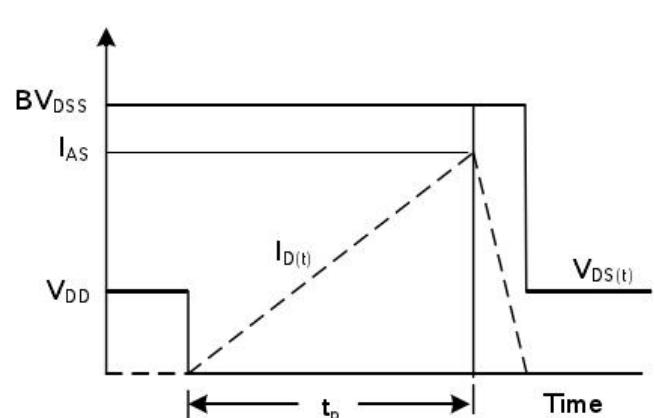


Gate Charge Test Circuit

Gate Charge Waveform

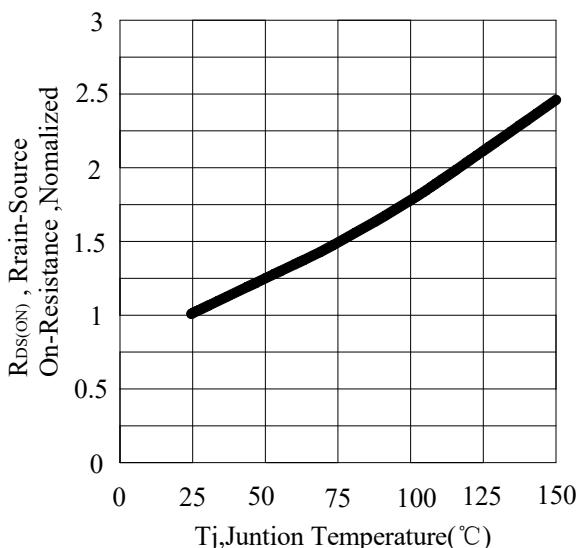
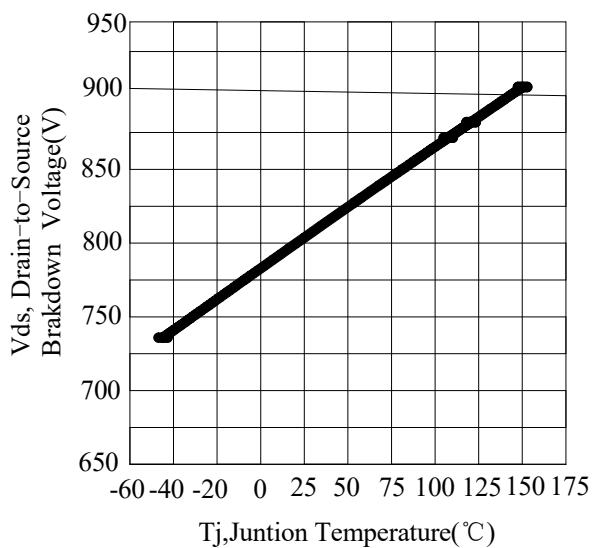
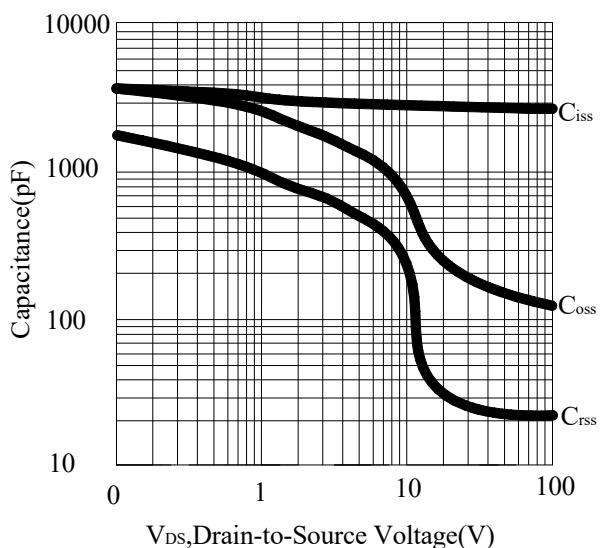
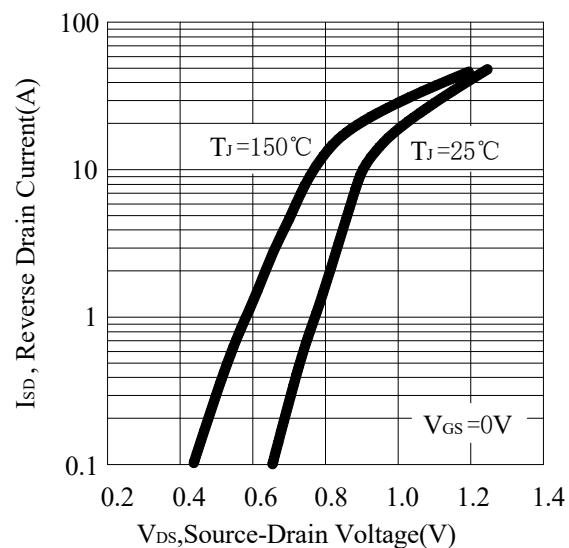
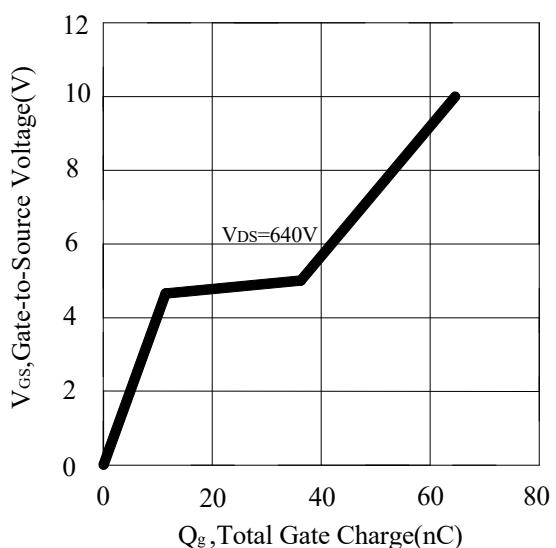
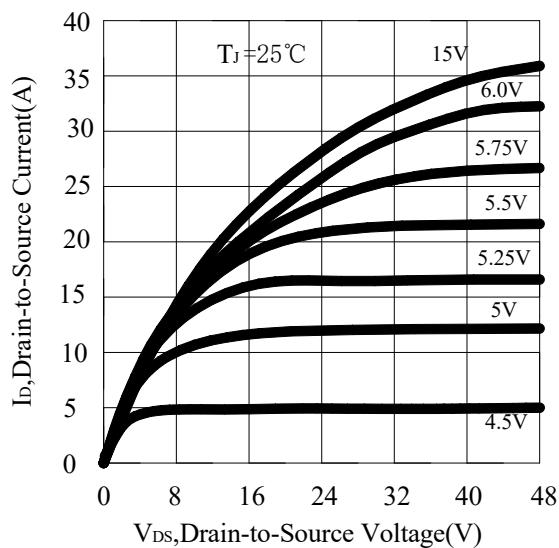


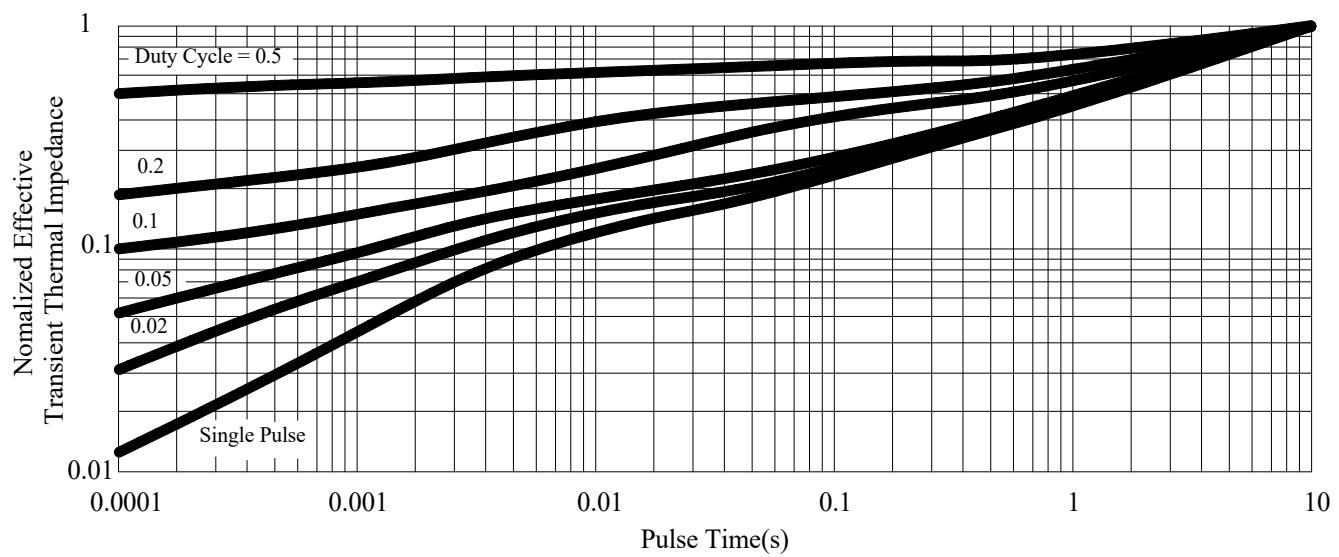
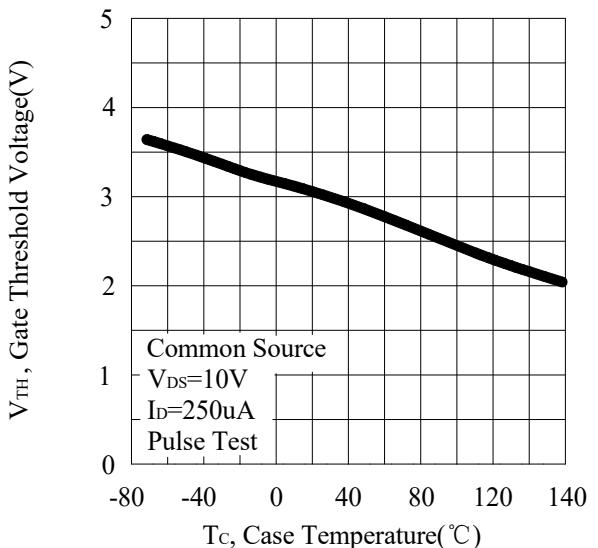
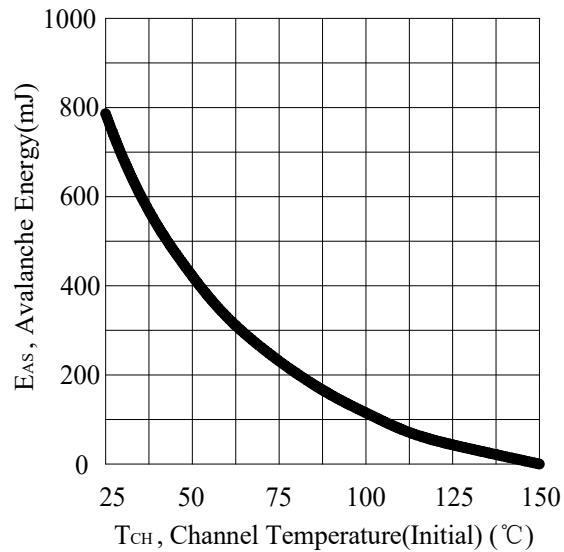
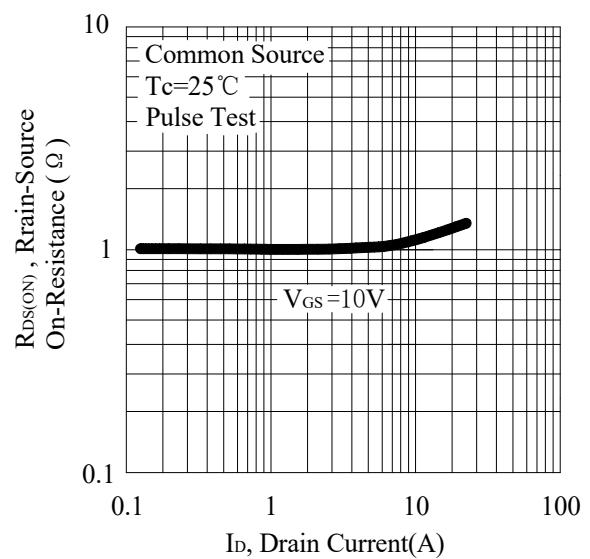
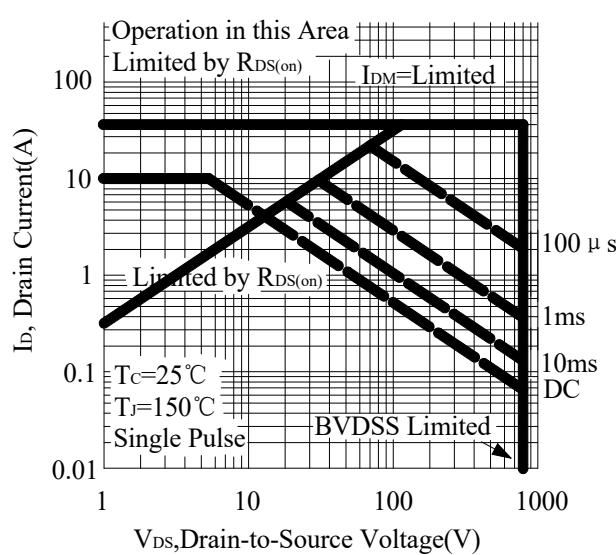
Unclamped Inductive Switching Test Circuit

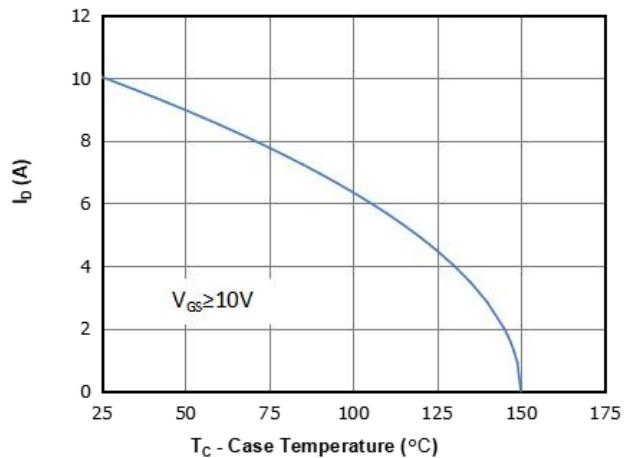


Unclamped Inductive Switching Waveforms

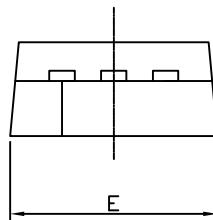
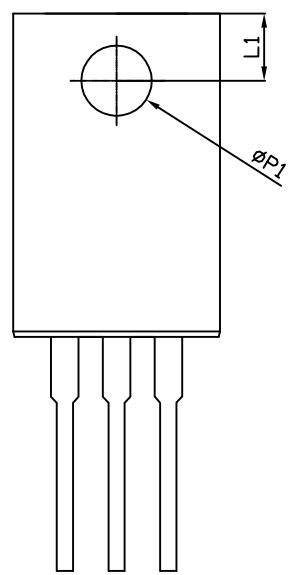
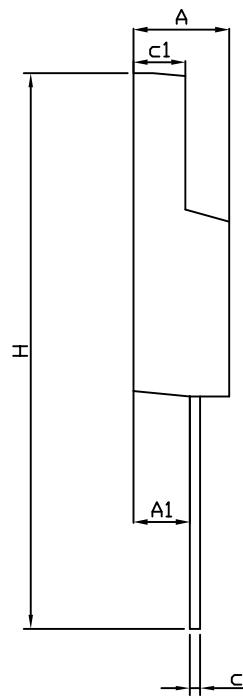
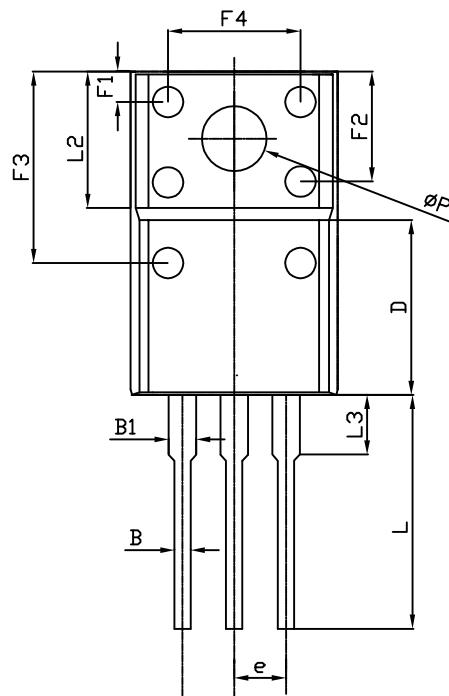
## RATING AND CHARACTERISTIC CURVES



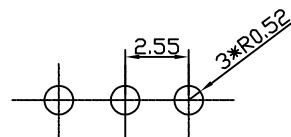




# TO-220F-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.40	4.60	4.80
A1	2.63	2.76	2.89
B	0.75	0.80	0.90
B1	1.12	1.27	1.42
c	0.40	0.50	0.60
c1	2.60	2.70	2.80
D	7.50	7.80	8.10
e	-	2.55REF	-
E	9.86	10.00	10.10
F1	1.90	2.12	2.40
F2	5.00	5.30	5.65
F3	8.70	9.00	9.30
F4	6.20	6.50	6.80
H	27.80	28.30	28.80
L	13.10	13.30	13.50
L1	2.85	3.00	3.15
L2	-	6.70REF	-
L3	2.80	3.10	3.40
ΦP	3.00	3.30	3.60
ΦP1	2.80	3.10	3.40