

**Features**

- Enhancement mode transistor-Normally off power switch
- No reverse-recovery charge
- Low gate charge, low output charge
- Ultra high switching frequency
- Qualified according to JEDEC for target applications

**Applications**

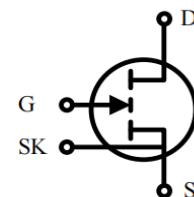
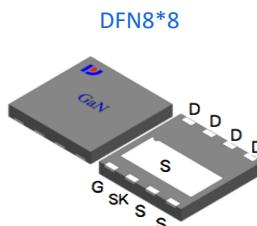
- AC-DC converters
- DC-DC converters
- Fast battery charging
- High density power conversion
- High efficiency power conversion

**Benefits**

- Enable very high conversion efficiencies
- Supports high operating frequency
- Enables ultrahigh power density designs
- Improved safety & reliability due to cooler operation temperature

**Product Summary**

$V_{DS}$	700V
$R_{DS(on)}$ @6.0V typ	102mΩ
$I_D$	17A

**Package Marking and Ordering Information**

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PWEG140N70W	EG140N70W	DFN8*8	Tape&Reel	13 inches	16mm	4000pcs

**Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-source voltage ( $T_j = -55^\circ C$ to $150^\circ C$ )	$V_{DSS}$	700	V
Drain to source voltage transient <sup>1</sup>	$V_{(TR)DSS}$	800	
Drain to source voltage,pulsed <sup>2</sup> $T_j = 25^\circ C$ ; total time < 10 h $T_j = 125^\circ C$ ; total time < 1 h	$V_{DSS,pulse}$	750	V
Continuous current, drain source	$I_D$	17	A
Pulsed current, drain source <sup>3</sup> $V_{GS} = 6V$ ; $T_{PULSE} = 10 \mu s$ ; $TC = 25^\circ C$ ; $V_{GS} = 6V$ ; $T_{PULSE} = 10 \mu s$ ; $TC = 125^\circ C$ ;	$I_{D,pulse}$	32 18	A
Gate source voltage, continuous <sup>4</sup> $T_j = -55^\circ C$ to $150^\circ C$	$V_{GS}$	-6~7	V
Gate source voltage, pulsed	$V_{GS,pulse}$	-20~10	V
Power dissipation	$P_{tot}$	113	W
Operating temperature	$T_j$	-55~150	°C
Storage temperature	$T_{stg}$		
Maximum reflow soldering temperature	$T_{sold}$	260	°C

1. $V_{DS}$ , transient is intended for non-repetitive events,  $t_{PULSE} < 200 \mu s$ .

2. $V_{DS}$ , pulse is intended for repetitive pulse,  $t_{PULSE} < 100 ns$ .

3.Limit was extracted from characterization test, not measured during production.

4.The minimum  $V_{GS}$  is clamped by ESD protection circuit, as shown in Figure 10.

**Thermal Resistance**

Parameter	Symbol	Limit value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – ambient	R <sub>thJA</sub>	-	65	-	°C/W	-
Thermal resistance, junction - case	R <sub>thJC</sub>	-	1.1	-	°C/W	-

**Electrical Characteristic (at T<sub>j</sub> = 25 °C, unless otherwise specified)**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

**Static characteristics**

Gate threshold voltage	V <sub>GS(th)</sub>	1.2 -	1.45 1.45	2.5 -	V	I <sub>D</sub> =11mA, V <sub>DS</sub> =V <sub>GS</sub> T <sub>j</sub> =25°C T <sub>j</sub> =150°C
Drain-to-source leakage current	I <sub>DSS</sub>	- -	0.6 7	20 -	μA	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V T <sub>j</sub> =25°C T <sub>j</sub> =150°C
Gate-source leakage current	I <sub>GSS</sub>	-	70	-	μA	V <sub>GS</sub> =6V, V <sub>DS</sub> =0V
Drain-source on-state resistance	R <sub>DS(on)</sub>	-	102	140	mΩ	V <sub>GS</sub> =6V, I <sub>D</sub> =5A, T <sub>j</sub> =25°C
		-	230	-	mΩ	V <sub>GS</sub> =6V, I <sub>D</sub> =5A, T <sub>j</sub> =125°C
Gate resistance	R <sub>G</sub>	-	6.8	-	Ω	f = 1 MHz; open drain

**Dynamic characteristics**

Input Capacitance	C <sub>iss</sub>	-	122	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =400V, f=100KHz	
Output Capacitance	C <sub>oss</sub>	-	45	-			
Reverse Transfer Capacitance	C <sub>rss</sub>	-	0.9	-			
Effective output capacitance, energy related <sup>1</sup>	C <sub>o(er)</sub>	-	59	-			
Effective output capacitance, time related <sup>2</sup>	C <sub>o(tr)</sub>	-	82	-			
Output charge	Q <sub>oss</sub>	-	33	-	nC	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V~400V, See Figure 22	
Turn-on delay time	t <sub>d(on)</sub>	-	3	-	ns		
Rise time	t <sub>r</sub>	-	5	-			
Turn-off delay time	t <sub>d(off)</sub>	-	4	-			
Fall time	t <sub>f</sub>	-	4	-			

1. C<sub>O(er)</sub> is the fixed capacitance that gives the same stored energy as C<sub>oss</sub> while V<sub>DS</sub> is rising from 0 to 400 V.

2. C<sub>O(tr)</sub> is the fixed capacitance that gives the same charging time as C<sub>oss</sub> while V<sub>DS</sub> is rising from 0 to 400 V.

**Gate charge characteristics**

Gate Total Charge	$Q_G$	-	3.5	-	nC	$V_{DS}=400V, I_D=5A$ , $V_{GS}=0V-6V$
Gate-Source charge	$Q_{GS}$	-	0.3	-		
Gate-Drain charge	$Q_{GD}$	-	1.2	-		
Gate Plateau Voltage	$V_{Plat}$	-	2.1	-	V	$V_{DS}=400V, I_D=5A$

**Reverse Device Characteristic**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	$V_{SD}$	-	2.4	-	V	$V_{GS}=0V, I_S=5A$
Pulsed current, reverse	$I_{S,pulse}$	-	-	32	A	$V_{GS}=6V, t_{PULSE}=10\mu s$
Reverse recovery charge	$Q_{rr}$	-	0	-	nC	$I_S=5A, V_{DS}=400V$
Reverse recovery time	$t_{RR}$	-	0	-	ns	
Peak reverse recovery current	$I_{rrm}$	-	0	-	A	

### Typical Performance Characteristics

Fig 1: Typ. Output Characteristics

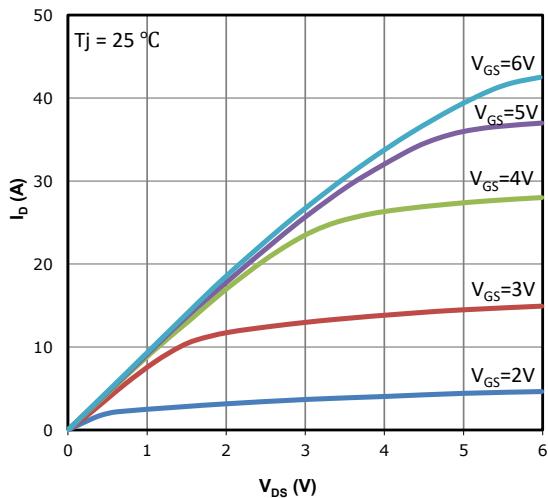


Fig 2: Typ. Output Characteristics

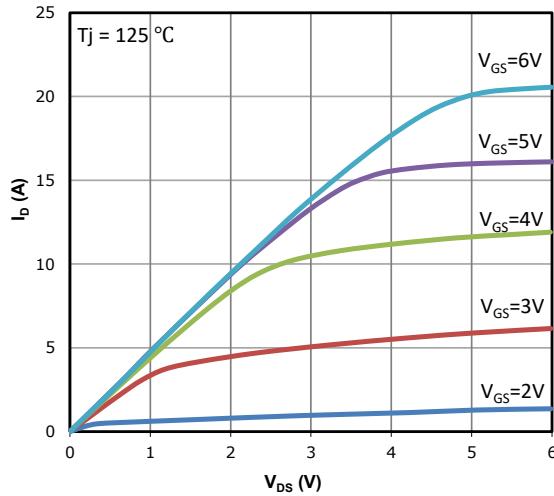


Fig 3:  
Typ.Drain-source on-state resistance

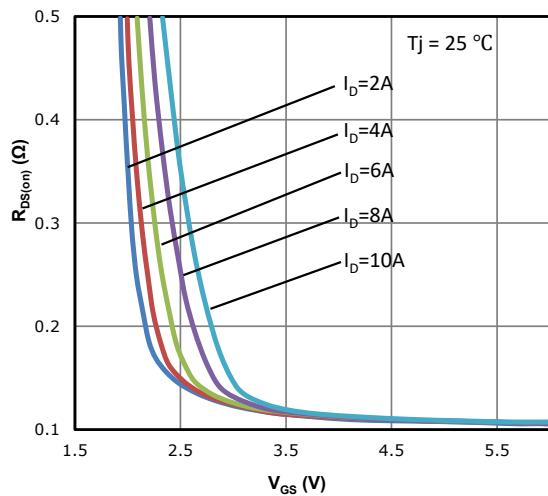


Fig 4:  
Typ.Drain-source on-state resistance

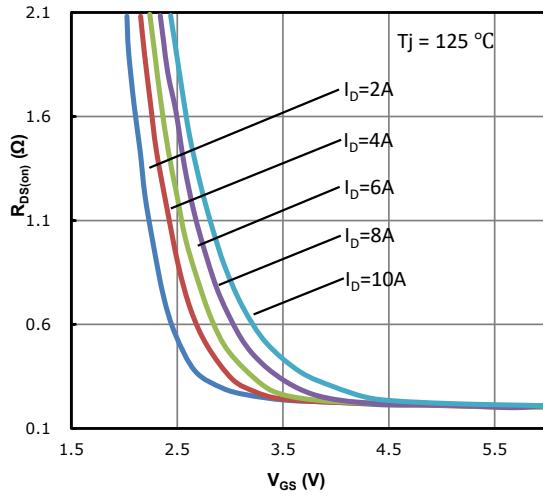


Fig 5:  
Typ. channel reverse characteristics

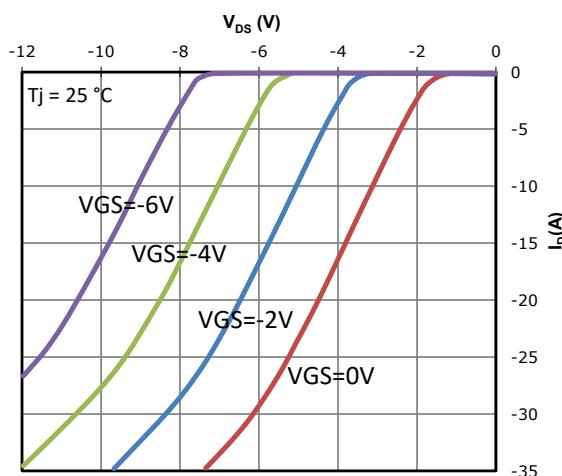


Fig 6:  
Typ. channel reverse characteristics

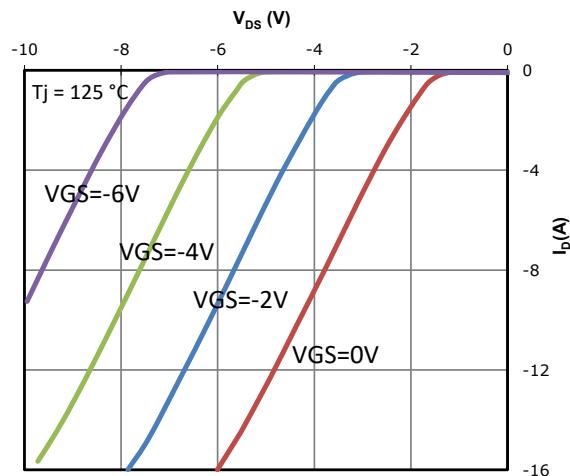


Fig 7:  
Typ. channel reverse characteristics

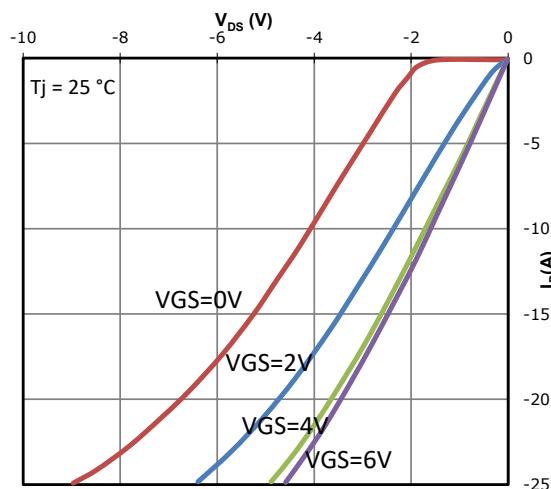


Fig 8:  
Typ. channel reverse characteristics

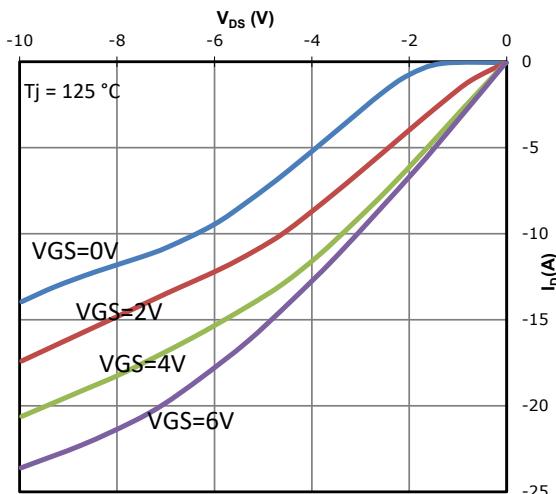


Fig 9: Typ. Transfer Characteristics

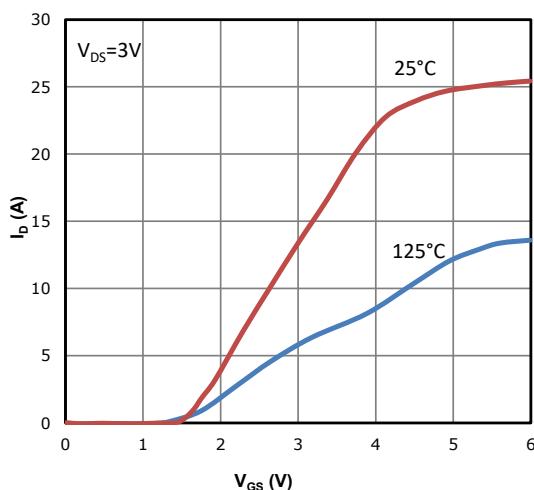


Fig 10: Typ. Gate-to-Source leakage

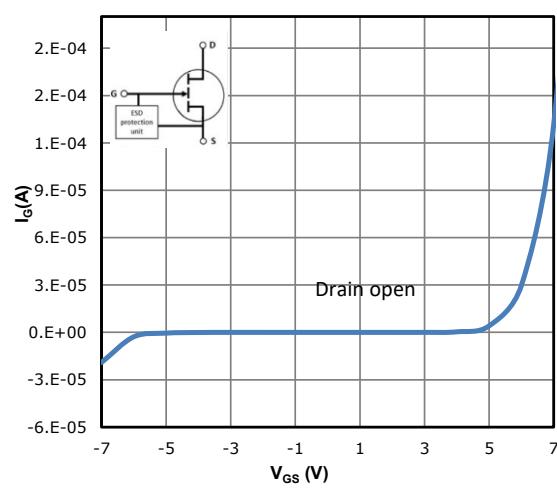


Fig 11: Drain-source leakage characteristics

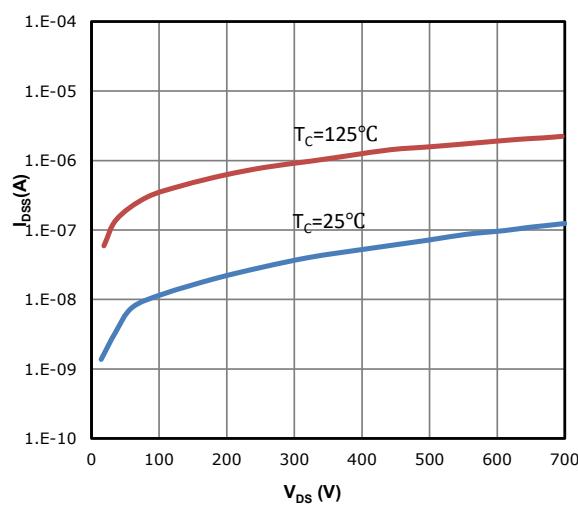


Fig 12:  $V_{GS(th)}$  vs. Temperature

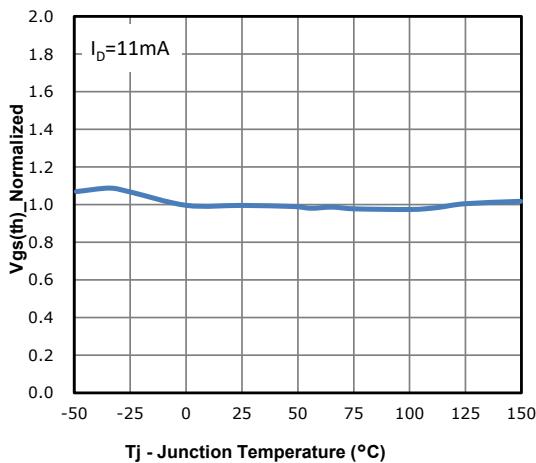


Fig 13:  $R_{DS(on)}$  vs. Temperature

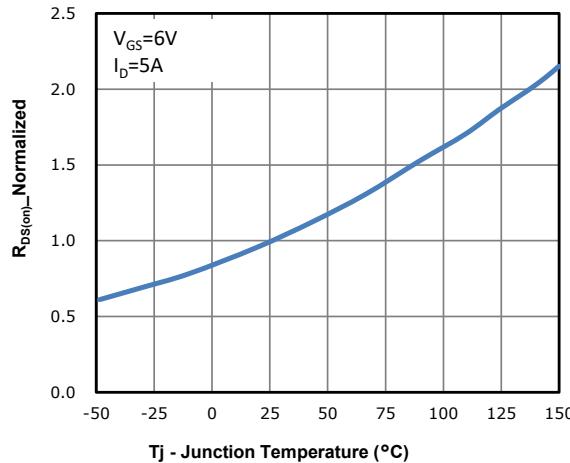


Fig 14: Power Dissipation

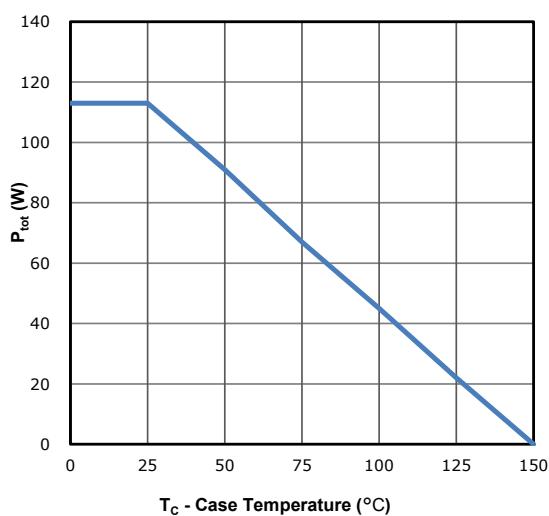


Fig 15:  
Max. Transient Thermal Impedance

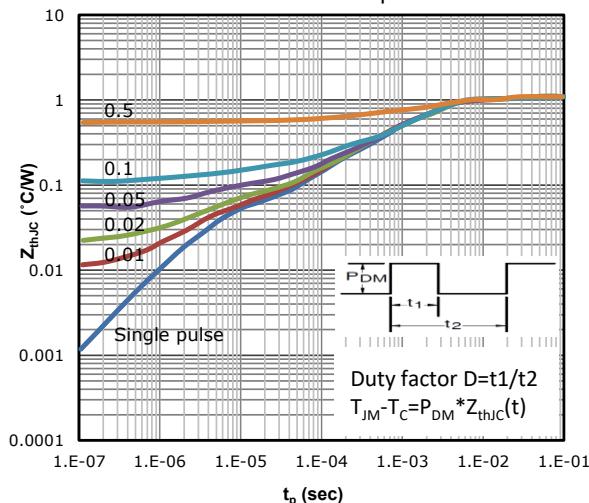


Fig 16: Safe Operating Area

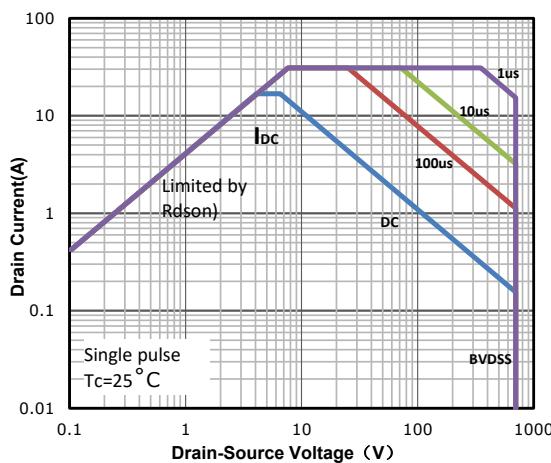


Fig 17: Safe Operating Area

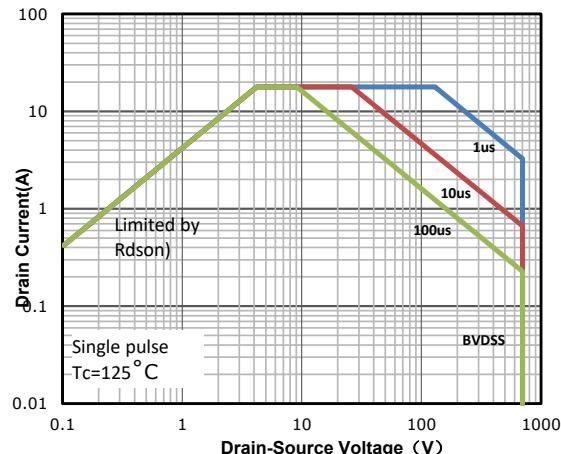


Fig 18: Gate Charge Characteristics

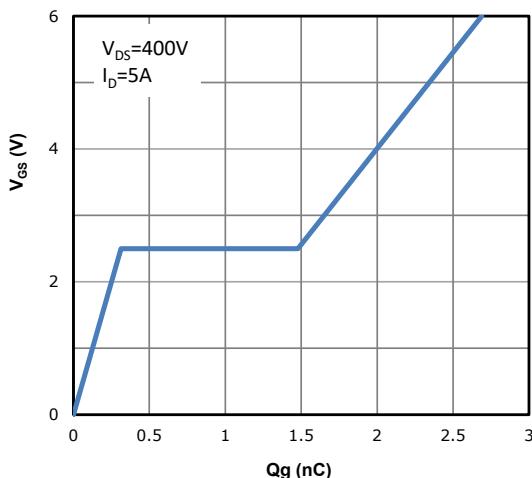


Fig 19: Capacitance Characteristics

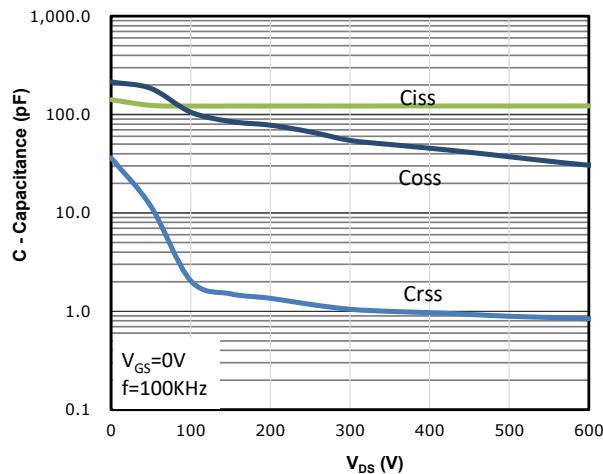


Fig 20: Typ. output charge

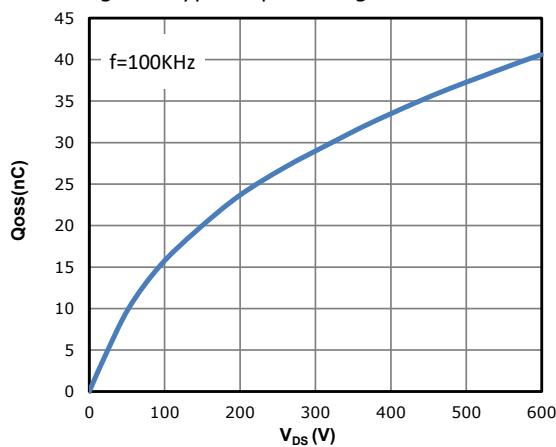
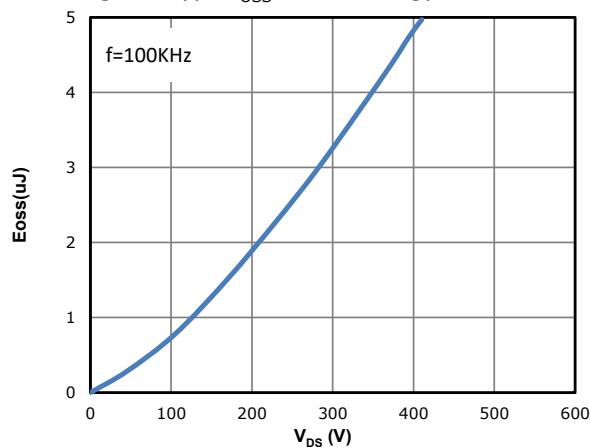
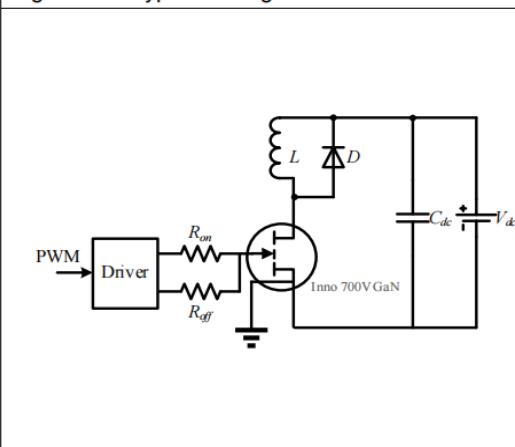


Fig 21: Typ.  $C_{oss}$  stored Energy



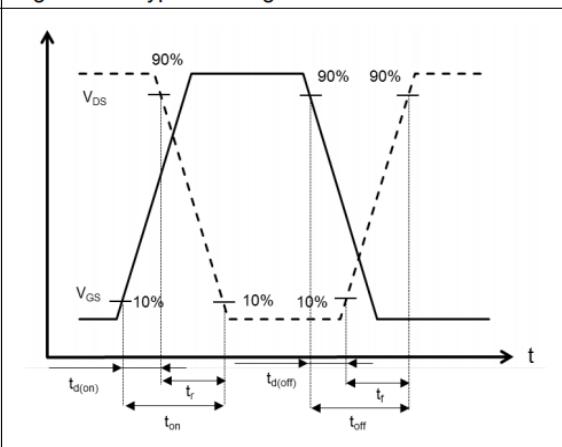
### Test Circuit & Waveform

Figure 22 Typ.Switching times with inductive load

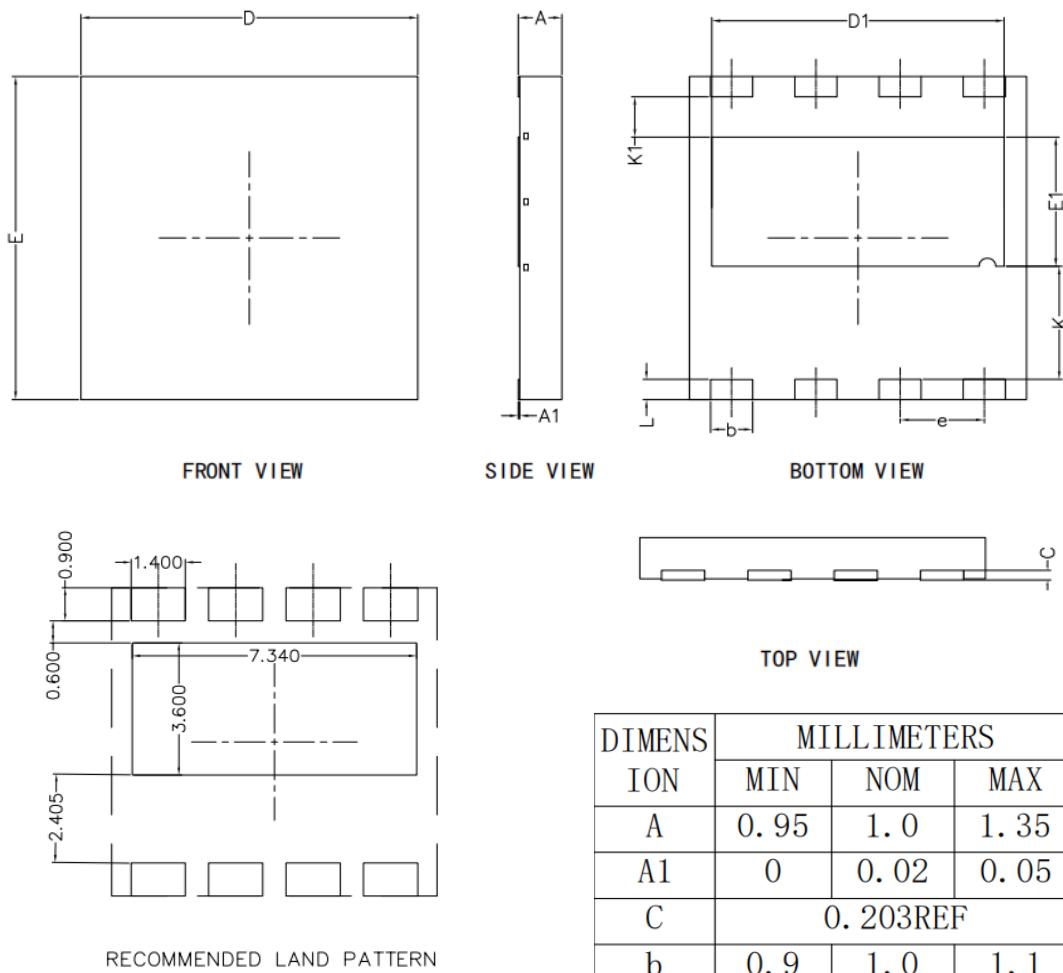


$V_{DS} = 400 V$ ,  $I_D = 10 A$ ,  $L = 318 \mu H$ ,  $V_{GS} = 6 V$ ,  
 $R_{on} = 10 \Omega$ ,  $R_{off} = 2 \Omega$

Figure 23 Typ.Switching times waveform



## Package Outline: DFN8\*8



DIMENSION	MILLIMETERS		
	MIN	NOM	MAX
A	0.95	1.0	1.35
A1	0	0.02	0.05
C	0.203REF		
b	0.9	1.0	1.1
D	7.9	8.0	8.1
D1	6.84	6.94	7.04
E	7.9	8.0	8.1
E1	3.1	3.2	3.3
e	2.0BSC		
K	2.8REF		
K1	0.9	1.0	1.1
L	0.45	0.5	0.55

**Disclaimer**

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Buyer is responsible for its products and applications using PingWei products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by PingWei.

"Typical" parameters which may be provided in PingWei data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts

**ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE**